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A Modular Approach to Large-Signal Modeling of an Interconnected AC/MTDC System

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Abstract—The so-called modular modeling of interconnected AC/MTDC (Multi-Terminal DC) systems aids their synthesis and analysis by decomposing it into structural modules (e.g. voltage-source converter, synchronous generator, AC line, DC line) and by exploring the patterns of interconnection that yield the overall performance of a system. In this paper, we introduce our idea on the modular medaling of large streng dynamics our idea on the modular modeling of large-signal dynamics for an interconnected AC/MTDC system. Effectiveness of the idea is shown with numerical simulations for a relatively simple configuration of three-terminal DC grid.

Index Terms—Multi-terminal direct current; Large-signal

modeling; Numerical simulation; Power system performance

assessment

I. Introduction

Multi-Terminal DC (MTDC) grids based on Voltage-Source Converters (VSCs) have been widely studied: see, e.g., [1]. By virtue of the control capability of VSCs, the MTDC grids are expected to provide flexibility in the operation of conventional AC grids. Most of practical MTDC grids are now in the planning phase, therefore it is relevant to rely on the so-called model-based approach to synthesis and analysis of static/dynamic performances for interconnected systems of AC/MTDČ grids. For this, it is required to construct a generic mathematical model capturing the physical characteristics of a target MTDC grid and to integrate the model with conventional models of the AC grids for exploring various patterns of interconnection of our interest. No general framework of the integration applicable to a wide class of the AC/MTDC interconnection has been reported yet to the best of the authors'

The so-called modular modeling of interconnected AC/MTDC systems aids their synthesis and analysis by decomposing it into structural modules (e.g. VSC, synchronous generator, AC line, DC line) and by exploring the patterns of interconnection that yield the overall performance of a system such as economic efficiency (optimality) and physical stability. In this paper, we introduce our idea on the modular modeling of large-signal dynamics for a relatively-simple model of interconnected AC/MTDC systems. The large-signal dynamics and associated stability have a long of history of research on interconnected AC/DC systems: see, e.g., [2]–[4]. The modular modeling is motivated by not only software development for generic use [5] but also system-theoretic perspective [6], [7]. For the modular modeling, in this paper we reformulate existing dynamic models of a synchronous generator and a DC line (phasorbased for AC and instantaneous variable-based for DC), called modules, so that they can be utilized in the modular modeling. In addition to the reformulation, by virtue of the switching function to represent the ON/OFF operation of IGBT valves,

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which is utilized in [4] for current-source converters, we derive a basic mathematical model of VSC that represents its power conversion with a simple control mechanism, which is expected to be minimal for the large-signal studies. The power-based approach makes it possible to explore the patterns of interconnection of various components in AC and MTDC grids, in order to assess the overall largesignal dynamics and stability. Effectiveness of the modular modeling is examined with numerical simulations for multiple configurations of the MTDC network. A preliminary version of this paper is the non-reviewed technical report [8] inside Japan that includes the list of parameter values used in this

There are several works related to the contents of this paper. In [9], a generic model for VSC-based MTDC consisting of converter and its controllers is proposed. In [10], a combination of the linearized classical swing equation and DC circuit equations with PI-type converter controller is used for design of distributed frequency controller. In [11], the sixthorder model for generator dynamics with models of MMC (Modular Multilevel Converter) and DC cable are introduced for assessment of small-signal stability. In the present paper, we derive a relatively simple model of VSC by a common formula of power conversion in [9] and thus a set of nonlinear differential and algebraic equations for the large-signal dynamics and stability.

II. SYSTEM MODEL

Figure 1 shows a single-line diagram of the interconnected system of AC/MTDC grids for which we demonstrate the modular modeling. The system model includes the two AC grids (left and right sides of the figure) asynchronously operated onto one MTDC grid like [11]. The left AC grid has one synchronous generator and two VSCs, called VSC#1 and VSC#2, and the grid's dynamics and stability are of basic interest for the large-signal modeling. The right AC grid has one VSC, called VSC#3, and no generator; thus the associated dynamics are not considered. Each of the AC grids includes the infinite bus as a synchronously-rotating reference axis. In the MTDC network, we suppose from wide use in practical systems that it is of bipolar-type with a return conductor [12], as shown in Section III-C.

III. MODELING FRAMEWORK: MODULE MODELS AND THEIR INTERCONNECTION

This section describes the modular approach to largesignal modeling of the interconnected system in Figure 1. Specifically, we reinterpret the standard models of dynamics of synchronous generator, AC and DC line as *modules*, and we derive a basic model of VSC as an essential element connecting the AC and MTDC grids. Here, we make the following three assumptions:

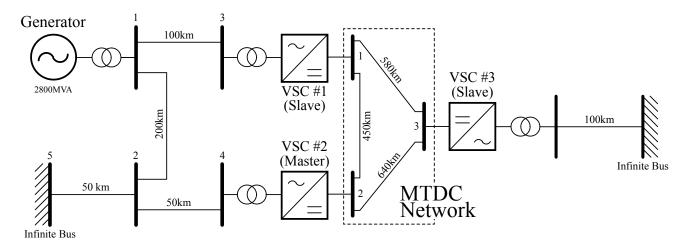


Fig. 1. Single-line diagram of the interconnected system of AC/MTDC grids for which we demonstrate the modular modeling in this paper

- A1 For the AC grids, we focus on the timescale of electromechanical dynamics (normally, 0 to 10 seconds) that is essential in the large-signal studies. Any electrical transient in AC lines and transformers are thus negligible. In addition, three-phase voltages are balanced in steady and transient states.
- A2 For the bipolar-type MTDC network, voltages and currents in positive and negative-side circuits are balanced in steady and transient states.
- A3 For the three VSCs, no power loss exists, and switching operations of IGBT-based valves are ideal.

Assumption A1 is again standard in the large-signal studies. Assumptions A2 and A3 are used for characterizing electromechanical dynamics of synchronous generators operating onto both AC and DC grids [4], [13]. All the variables and parameters are in per-unit systems for the AC and MTDC grids: see [13] in details.

A. Synchronous Generator

We represent the electromechanical dynamics of the synchronous generator in Figure 1 using the so-called one-axis model with exciter [14]. For the generator, we denote δ by the angular position of rotor with respect to the infinite bus. The non-dimensional deviation of rotor speed relative to a nominal angular frequency is denoted by ω , and the non-dimensional voltage behind transient reactance by e_q' . The model is then given as

$$\frac{\mathrm{d}\delta}{\mathrm{d}t} = \omega$$

$$2H\frac{\mathrm{d}\omega}{\mathrm{d}t} = -D\omega + P_{\mathrm{m}} - p$$

$$T'_{d0}\frac{\mathrm{d}e'_{q}}{\mathrm{d}t} = \frac{X_{d} - X'_{d}}{X'_{d}}v_{1}\cos(\delta - \theta_{1}) + E_{fd} - \frac{X_{d}}{X'_{d}}e'_{q}$$
(1)

where $H,D,T'_{d0},X_d\left(X_q\right),X'_d$, and E_{fd} are parameters of the generator. The mechanical input power $P_{\rm m}$ is constant under Assumption A1. The electric output of active power p and

associated reactive power q are given by

$$p = \frac{e'_q v_1}{X'_d} \sin(\delta - \theta_1) + \frac{v_1^2}{2} \frac{X'_d - X_q}{X'_d X_q} \sin 2(\delta - \theta_1)$$

$$q = \frac{e'_q v_1}{X'_d} \cos(\delta - \theta_1) - \frac{v_1^2}{2} \frac{X'_d + X_q}{X'_d X_q}$$

$$+ \frac{v_1^2}{2} \frac{X'_d - X_q}{X'_d X_q} \cos 2(\delta - \theta_1)$$
(2)

The variables θ_1 and v_1 are the phase and amplitude of voltage phasor at AC bus #1 onto which the generator is operated. We state that the 4-tuple (p,q,θ_1,v_1) is the set of variables sharing to express interconnections [6], that is, to link the generator's model ("module") with the AC lines below, which is conventional in power system studies.

B. AC Lines

From Assumption A1, we simply represent AC lines in Figure 1 using the power-flow model. For two AC buses in the left grid, which are labeled by integers i and j from $\{1, \ldots, 5\}$ and connected by AC lines, we have

$$\left. \begin{array}{l}
 p_{i} = v_{i}v_{j}\{G_{ij}\cos(\theta_{i} - \theta_{j}) + B_{ij}\sin(\theta_{i} - \theta_{j})\} \\
 + v_{i}^{2}G_{ii} \\
 q_{i} = v_{i}v_{j}\{G_{ij}\sin(\theta_{i} - \theta_{j}) - B_{ij}\cos(\theta_{i} - \theta_{j})\} \\
 - v_{i}^{2}B_{ii}
 \end{array} \right\}$$
(3)

where θ_i and v_i are the phase and amplitude of voltage phasor at AC bus #i, and G_{ij} and B_{ij} are parameters of the AC lines. The active power p_i and reactive power q_i are terms determined by a generator, load, and VSC connected to AC bus #i. The 4-tuple (p,q,θ_i,v_i) is hence the set of variables sharing to express interconnections, namely, to link the AC-line module with another.

C. DC Lines

A π -type equivalent circuit of bipolar DC lines is shown in Figure 2, where $R_{\rm dc\,line}$, $L_{\rm dc\,line}$, and $C_{\rm dc\,line}$ are constant parameters. The ports on the left and right sides of the figure correspond to DC buses for which we use the integer labels i and j. Under Assumption A2, in order to consider a possible fault in the DC network and associated electrical transient, we

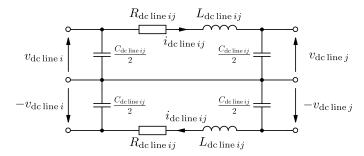


Fig. 2. π -type equivalent circuit of bipolar-type (symmetrical) DC lines

use the governing equations for the DC current $i_{{
m dc\,line}\,ij}$ and the DC voltage $v_{{
m dc\,line}i}$ as

$$L_{\text{dc line } ij} \frac{\text{d}i_{\text{dc line } ij}}{\text{d}t} = -R_{\text{dc line } ij}i_{\text{dc line } ij} + v_{\text{dc line } i} + v_{\text{dc line } i}$$

$$C_{\text{dc line } i} \frac{\text{d}v_{\text{dc line } i}}{\text{d}t} = i_{\text{dcc} i} - i_{\text{dc line } ij}$$

$$(4)$$

where $i_{\rm dcci}$ stands for the DC current flowing into DC bus #i from an AC grid through VSC and will be a key variable below. This shows that the tuple $(v_{\rm dc\,linei},i_{\rm dcci})$ is the set of variables sharing to express interconnections, that is, to link the DC-line module with another. It is noted that if DC bus #i has no VSC, then the associated $i_{\rm dcc}i$ is negligible.

D. Voltage-Source Converters (I)—Physical

Above, we introduced the modules for AC generator and AC lines with the variables (p,q,θ,v) and the module for DC lines with $(v_{\rm dc},i_{\rm dc})$. To *share* their variables at a VSC, we now derive a basic model of VSC based on the conservation law of power.

First, we consider the relation between voltages on AC and DC sides at a VSC. Let us denote by e_d and e_q the d-and q-axis voltages at the AC side 1 , and by $v_{\rm dc}$ the positive-side voltage at the DC side which magnitude is equal to the negative-side voltage under Assumption A2. The $v_{\rm dc}$ can be identified as $i_{\rm dc\,line}i$ in Figure 2. Needless to say, e_d and e_q are functions of the phase θ and amplitude v of voltage phasor at the corresponding AC bus. By focusing on the harmonic component of voltage waveform at the AC side through the switching function, the following approximate relation among e_d , e_q , and $v_{\rm dc}$ is derived in [8]:

$$e_d = 2K_V v_{\rm dc} u_d, \quad e_q = 2K_V v_{\rm dc} u_q, \tag{5}$$

with the two control variables

$$u_d := \frac{2}{\pi} \sin \frac{\alpha}{2} \sin \theta, \quad u_q := -\frac{2}{\pi} \sin \frac{\alpha}{2} \cos \theta,$$
 (6)

or

$$v := \sqrt{e_d^2 + e_q^2} = 2K_V v_{dc} u$$

$$u := \sqrt{u_d^2 + u_q^2} = \frac{2}{\pi} \sin \frac{\alpha}{2}$$
(7)

where $\alpha \in [0,\pi]$ stands for the conduction duration of IGBT valves of VSC, and θ for the phase of voltage phasor at the AC side. The constant K_V in (5) and (7) is determined by the choice of base quantities in the per-unit systems of AC and MTDC grids: see [13] in details.

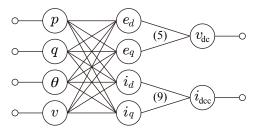


Fig. 3. Graphical representation of sharing the variables at Voltage-Source Converter (VSC) $\,$

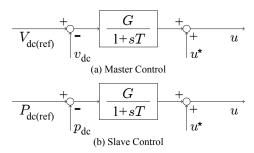


Fig. 4. Block diagrams of Master-Slave Control for Voltage-Source Converter (VSC)

Second, we consider the relation between currents at the VSC. From Assumption A3, by denoting as i_d and i_q the d-and q-axis currents at the AC side, the conservation law of active power in the per-unit systems yields

$$p = e_d i_d + e_q i_q = v_{dc} i_{dcc} \tag{8}$$

where $i_{\rm dcc}$ appears in (4). Thus, from (5), the following relation among i_d , i_q , and $i_{\rm dcc}$ is derived in [8]:

$$\sqrt{3}K_I(u_d i_d + u_g i_g) = i_{dcc} \tag{9}$$

where we note $\sqrt{3}K_I$ is the constant determined by the perunit systems. The i_d and i_q are determined with (e_d,e_q,p) and $q=e_qi_d-e_di_q$ [15]. Hence, the static VSC model is established as the power conversion (8) and the power-based conversion of variables in Figure 3.

E. Voltage-Source Converters (II)—Control

Finally, we introduce a control model of VSC. There is a lot of research and development of control systems for VSCs: see, e.g., [12], [16]. It is valuable to explore which type of control mechanism is minimal for explaining the target large-signal dynamics and stability; this is an open question. In this paper, as a preliminary work, we adopt the idea of Master-Slave Control that is investigated in practice [17] and studied in [18]. In this, one VSC, called Master-VSC, aims to regulate a DC voltage while another, called Slave-VSC, to regulate active/reactive power. By assuming that internal control dynamics are sufficiently fast and thus negligible in the timescale of electromechanical dynamics, we consider the Master-Slave Control as shown in Figure 4, where u in (7) is the control variable, u^* its steady-state value, and $V_{\text{dc(ref)}}$ (or $P_{\rm dc(ref)}$) the set-point of voltage (or power) regulation at a Master-VSC (or Slave-VSC). The first-order lag element represents the inevitable lag effect of signals in measurement and actuation. Needless to say, there is room for improvement of the modeling in terms of large-signal dynamics and stability. One idea is to incorporate the so-called droop-type control

¹Under Assumption A1, there is no zero-axis voltage.

(see, e.g., [19]) to the current model, which is in our future research.

F. Model Development by Interconnection

By connecting the above modules, we are able to develop a nonlinear dynamic model for the interconnected system of AC/MTDC grids. To this end, we need to modify the module models of AC lines and DC lines according to the graph structures of AC and MTDC transmission networks, that is to say, how the module models are connected in the target system.

For simplicity of the presentation, we now focus in Figure 1 on the left AC grid and MTDC grid: the following modification holds for the right AC grid. Now, following [7], [10], let us represent the AC and MTDC networks using directed graphs $\mathcal{G}_{AC} = (\mathcal{V}_{AC}, \mathcal{E}_{AC})$ and $\mathcal{G}_{DC} = (\mathcal{V}_{DC}, \mathcal{E}_{DC})$ with the set of AC (or DC) buses $\mathcal{V}_{AC} = \{1, 2, \dots, 5\}$ (or $\mathcal{V}_{DC} = \{1, 2, 3\}$) and AC lines $\mathcal{E}_{AC} \subseteq \mathcal{V}_{AC} \times \mathcal{V}_{AC}$ (or DC lines $\mathcal{E}_{DC} \subseteq \mathcal{V}_{DC} \times \mathcal{V}_{DC}$). Regarding software utilization, the graph structures \mathcal{G}_{AC} and \mathcal{G}_{DC} can be recursively updated by users (modelers) who analyze interconnected systems of interest. For each update, the modules of AC and DC lines are modified in the following manner.

The following modification of the module of AC lines is well-known in the traditional power-flow analysis. For this, the neighbors of AC bus #i are denoted by $\mathcal{N}_{ACi} := \{j \in \mathcal{V}_{AC} : (i,j) \in \mathcal{E}_{AC}\}$. Then, the power-flow model (3) is modified as follows:

$$p_{i} = \sum_{j \in \mathcal{N}_{ACi}} v_{i}v_{j} \{G_{ij}\cos(\theta_{i} - \theta_{j}) + B_{ij}\sin(\theta_{i} - \theta_{j})\}$$

$$+ v_{i}^{2}G_{ii}$$

$$q_{i} = \sum_{j \in \mathcal{N}_{ACi}} v_{i}v_{j} \{G_{ij}\sin(\theta_{i} - \theta_{j}) - B_{ij}\cos(\theta_{i} - \theta_{j})\}$$

$$- v_{i}^{2}B_{ii}$$

$$(10)$$

Also, if AC bus #i is connected to a VSC, then the corresponding voltage amplitude v_i is related to the DC-bus voltage with (7).

(7). We next show a similar modification of the module of DC lines. The neighbors of DC bus #i are denoted by $\mathcal{N}_{\mathrm{DC}i} := \{j \in \mathcal{V}_{\mathrm{DC}} : (i,j) \in \mathcal{E}_{\mathrm{DC}} \}$. For DC bus #i, by considering the smoothing capacitance $C_{\mathrm{dc\;conv}}$ of VSC, the total capacitance $C_{\mathrm{dc}i}$ is defined as

$$C_{\text{dc}i} := C_{\text{dc conv}} + \sum_{j \in \mathcal{N}_{\text{DC}i}} \frac{C_{\text{dc line }ij}}{2}.$$
 (11)

Thus, the circuit dynamics described by (4) are modified as follows:

$$L_{\text{dc line }ij} \frac{\text{d}i_{\text{dc line }ij}}{\text{d}t} = -R_{\text{dc line }ij}i_{\text{dc line }ij} + v_{\text{dc line }i} - v_{\text{dc line }j}$$

$$+ v_{\text{dc line }i} - v_{\text{dc line }ij}$$

$$C_{\text{dc}i} \frac{\text{d}v_{\text{dc}i}}{\text{d}t} = i_{\text{dcc}i} - \sum_{j \in \mathcal{N}_{\text{DC}i}} i_{\text{dc line }ij}$$
(12)

Therefore, we are able to build up the full model of the interconnected AC/MTDC system as a set of differential and algebraic equations described by (1,12) and (2,8,10), with the control mechanism of VSC in Figure 4.

It should be noted that the above modular modeling is explained for the simple system in Figure 1, and that the same modeling is applicable to more complicated systems without any substantial modification.

IV. NUMERICAL SIMULATIONS

In this section, we use the modular modeling for numerical simulations of large-signal dynamics for the interconnected AC/MTDC system in Figure 1. To show the effectiveness of the modular modeling, transient simulations for multiple configurations of the MTDC network were conducted as described below. The change of the configuration is easily realized in the modular modeling by updating the graph data \mathcal{G}_{DC} . The values of parameters for the AC and MTDC grids are based on standard setting in literature, e.g., [4], [13] and available in our technical report [8]. The control parameters of the three VSCs are G=-1, $T=1\,\mathrm{ms}$, $V_{\mathrm{dc(ref)}}=1$ for VSC#2, $P_{\mathrm{dc(ref)}}=0.2$ for VSC#1, and $P_{\mathrm{dc(ref)}}=-0.3$ for VSC#3. Unfortunately, there is no space in this paper that shows the complete list of the values of parameters. Numerical solutions of the set of nonlinear differential and algebraic equations were obtained with implicit trapezoidal method with fixed time-step of $1 \,\mathrm{ms}$. We supposed that at $t = 0 \,\mathrm{s}$ the system was at a normal operating condition (that is, a stable equilibrium point of the nonlinear dynamic model), and at $t = 0.1 \,\mathrm{s}$ a severe contingency occurred in the AC grid. The fault was encoded as a change of the initial condition from the equilibrium point: the value of ω at $t=0.1\,\mathrm{s}$ was increased by 0.02 (1 Hz).

Figure 5 shows the numerical simulations of the modular model for the four different configurations of the MTDC network. The four configurations are (a) all-to-all coupling, which is the same as in Figure 1, denoted by the black lines, (b) the MTDC network without line 1-2 denoted by the red lines, (c) the MTDC network without line 1-3 denoted by the green lines, and (d) the MTDC network without line 2–3 denoted by the blue lines. In this figure, the transient dynamics of the left AC grid are almost the same for the different configurations. This results from the setting that VSC#1 is close to the generator and works as Slave-VSC to regulate active power substantially like a constant power load. The swing frequency observed here is about 2 Hz and typical in the large-signal studies. The transient dynamics of the MTDC grid are dependent on the configurations. For example, the sign of the DC current $i_{
m dc\,line\,12}$ is changed for the green and blue lines (note that no red line appears in the same figure because no line 1-2 exists in this case). Here, we see that the transient dynamics of the DC voltage $v_{
m dc2}$ are almost the same because the voltage is regulated by Master-VSC. The transient dynamics of the other DC voltages $v_{\rm dc1}$ and $v_{\rm dc3}$ are dependent on the configurations. This is mainly from the fact that the two DC voltages are associated with Slave-VSC for active-power regulation. Therefore, we show that the relation between the configurations and system-wide dynamics are simulated and visualized in the modular modeling. In a case of DC fault, the system-wide dynamics become complicated and possibly multi-scale due to the bidirectional interaction of AC and MTDC grids. We will show this case in the conference presentation.

V. CONCLUDING REMARKS

This paper presented the first report on our modular approach to large-signal dynamics of interconnected AC/MTDC grids. The approach wad demonstrated numerically for the four different configurations of the three-terminal DC network. Future works are to improve the control model of VSC and to consider the relevance of the modular modeling in terms of large-signal performance assessment, e.g. by comparison with other modeling frameworks.

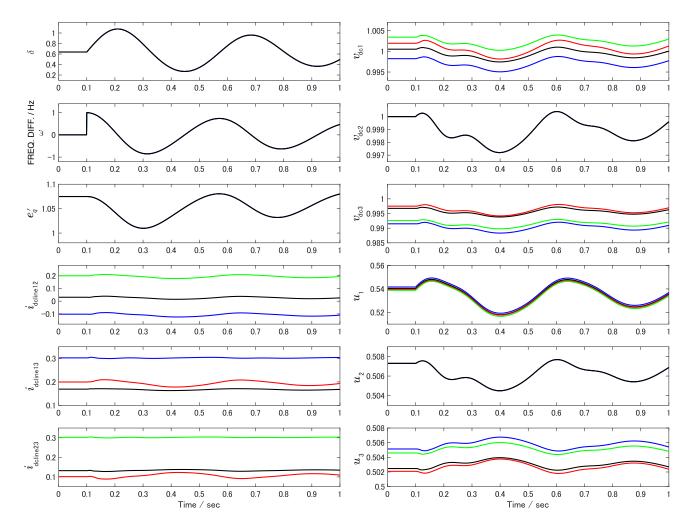


Fig. 5. Numerical simulations of large-signal dynamics in the interconnected system of AC/MTDC grids using the modular modeling: black for the MTDC network in Figure 1 with the three lines (i.e., all-to-all coupling); red for the MTDC network without line 1–2; green for the MTDC network without line 1-3; and blue for the MTDC network without line 2-3.

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