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Versatile functionality of four-terminal TiO_{2-x} memristive devices as artificial synapses for neuromorphic computing

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Abstract

Brain-inspired computing systems, which emulate the activity of biological synapses and neurons, are becoming more and more essential owing to their potential ability to solve the von Neumann bottleneck. Various types of memristive devices have been proposed to achieve information processing through synaptic functions. However, biological synapses have many complicated functions, such as hetero-synaptic plasticity and the related neuromodulation, that are difficult to implement in conventional two-terminal memristors because of the requirement for multiple inputs. In this study, simple four-terminal memristive devices consisting of epitaxial thin films of TiO_{2-x} are fabricated and their capacity to implement synaptic functions are explored. These devices utilize the two-dimensional variation of the oxygen vacancy distribution in the TiO_{2-x} film to realize advanced hetero-synaptic functionality, including tunable spike-timing-dependent plasticity, hetero-synaptic plasticity mimicking habituation and sensitization, and Pavlovian conditioning, through multi-terminal voltage inputs. The present results demonstrate the striking versatility of our four-terminal device for implementing the diverse and complex functions of artificial synapses on a single memristive passive element.

1. Introduction

Recent advances in artificial intelligence technology have attracted great attention. At the same time, they have highlighted a problem associated with present technologies known as the bottleneck of von-Neumann-architecture computers. One solution to this problem is to employ new computing architectures inspired by a biological brain. Neural systems in the brain and related biological cellular components are effective in handling complicated tasks with the action of synapses.¹⁻⁴ Consequently, artificial synapses, used to mimic the function of such bio-synapses, are crucial building blocks for constructing a hardware-based neural network in high-efficiency brain-inspired computing systems. Memristive devices have often been used as artificial synapses to closely emulate several neuromorphic properties,⁵⁻¹³ in which resistive switching, equivalent to the alteration of synaptic weight in neural systems, occurs in a non-volatile manner by applying external voltages to the devices.^{14,15} Reduced TiO₂ (TiO_{2-x}) is one promising candidate memristive material,¹⁶⁻¹⁸ its inherent oxygen vacancies acting as ionic donors contributing to resistive switching.

Recently, we have developed memristive devices with a unique four-terminal configuration using TiO_{2-x} single-crystal substrates.¹⁹⁻²¹ The four-terminal configuration is unique in that the resistance is determined by the two-dimensional distribution of oxygen vacancies in the electrically active zone, which can be controlled precisely by external voltages applied to the four terminal electrodes. Since oxygen vacancies have a positive charge, their distribution can be modulated through drift motion caused by the electric field in the device. This working principle widens the possibilities of mimicking higher-order brain and neuromorphic functions. The biological synapse has an intriguing function called hetero-synaptic plasticity, *i.e.*, modulatable plasticity induced by multiple connections of synapses and specific cellular mechanisms.²²⁻²⁵ This heterogeneity gives rise to learning functions such as associative learning and other types of behavioral learning.^{1,26,27} Although conventional two-terminal memristors have been employed so far for imparting hetero-synaptic properties in a number of complex manners,^{10,28-32} the multi-terminal configuration is essential for improving the versatility of hetero-synaptic functions.^{20,21,33-49}

In this study, we use conductive TiO_{2-x} epitaxial thin films grown on insulating TiO₂ substrates as a memristive material. The superiority of these epitaxial thin films, employed

in place of the previously used thermally-reduced substrates,^{20,21} is demonstrated on the basis of several memristive properties. Four-terminal devices are fabricated, consisting of two pairs of electrodes arranged diagonally, each pair having a different role: one pair is used as a homo-synapse for read/write operations, while the other pair is for gating operations corresponding to the action of the hetero-synapse and neuromodulator. The application of a gate voltage induces a two-dimensional modulation of the oxygen vacancy distribution, producing a rich variety of resistive switching properties and thus enabling higher-order neuromorphic functionality as the artificial hetero-synapse. Here we demonstrate the implementation of versatile behavior, such as spike-timing-dependent plasticity (STDP), habituation and sensitization, and Pavlovian conditioning known as associative learning, all of which become tunable with the help of heterogeneity functioning in a single memristive device.

2. Results and Discussion

2.1. Oxygen vacancy distribution and resistive switching

An optical micrograph of the four-terminal planar device formed with a TiO_{2-x} epitaxial thin film is shown in **Figure 1a**. We utilized (001) plane of rutile TiO_2 since this crystal orientation allows reversible change of two-dimensional oxygen vacancy distribution on applied voltages.^{20,21} The device has two pairs of diagonally opposed square Pt electrodes. To firstly examine the resistive switching performance of the device, electrical measurements were performed by applying voltages according to the sequence shown in **Figure 1b**. The conductance value G_{1-3} (G_{2-4}) between terminals T1 (T2) and T3 (T4), was measured while applying a voltage V_1 (V_2) to T1 (T2) as a read voltage while T3 (T4) was grounded. Subsequently, write voltages $V_{2,4}$ were simultaneously applied to both T2 and T4 for a certain period t while T1 and T3 were grounded. This cycle was repeated while ramping the voltage $V_{2,4}$ up and down over the range between the maximum voltage V_M and the minimum voltage V_m in 1 V steps. Since oxygen vacancies have a positive charge, it is expected that applying positive (negative) voltage to terminals T2 and T4 (i.e. $V_{2,4}$) concentrate oxygen vacancies around terminals T1 and T3 (T2 and T4), and make a bridge of conductive regions between T1 and T3 (T2 and T4), as will be shown later. This would

bring high G_{1-3} (G_{2-4}) and low G_{2-4} (G_{1-3}), and the conductance states should be inverted according to the changing $V_{2,4}$ voltage polarity.

Figure 2a (2b) shows variations of G_{1-3} (G_{2-4}) as a function of applied $V_{2,4}$, measured for the device having a TiO_{2-x} film with a thickness of 65 nm and a resistivity of $2.8 \times 10^{-3} \Omega \cdot \text{cm}$ with $V_M = +8 \text{ V}$ and $V_m = -8 \text{ V}$ for $t = 100 \text{ s}$, where the cycle was repeated six times. When G_{1-3} increases, G_{2-4} decreases, and vice versa, indicating that the G_{1-3} and G_{2-4} variations are complementary. This is a unique characteristic of our four-terminal device. With increasing $V_{2,4}$, a sharp increase (decrease) in G_{1-3} (G_{2-4}), a so-called SET (RESET) operation, occurs at +6 V, so that a high conductance state HCS (low conductance state LCS) is achieved. After the SET (RESET) operation occurs, with decreasing $V_{2,4}$, a sharp decrease (increase) in G_{1-3} (G_{2-4}) occurs at -6 V, so that a LCS (HCS) is achieved. In more detail, we observed that from HCS to LCS of G_{1-3} and G_{2-4} shows a region of gradual change, but not in the transition from LCS to HCS. This may be because on approaching HCS states, local oxygen concentration is at high level at which the conductivity change becomes more insensitive to concentration change, bringing abrupt resistive change near the saturation voltage. Whereas on approaching LCS states, oxygen concentration is at low level where conductivity change becomes more sensitive to concentration change, this would bring gradual change in resistivity near the saturation region. By applying the voltages cyclically, the conductance can be tuned repeatedly. The maximum HCS-to-LCS conductance ratio was found to be more than 20 in the present thin-film based device. This is a remarkable improvement over our previous devices, in which we used thermally reduced rutile- $\text{TiO}_2(001)$ single-crystal substrates and whose resistance ratio was 2 to 3.²⁰ Our obtained on/off ratio of 20 may be somewhat smaller than typically reported values for filamentary based memristors in which switching process is sharper but stochastic. Our device is based on bulk conducting mechanism rather than filamentary mechanism, aiming for improvement of yield and stability and resulting in moderate on/off ratio. Although further improvement of on/off ratio is desirable, tuning of multi-valued conductance states (synaptic weight) is possible in enough resolution as demonstrated in our previous report²¹ and results shown in later sections.

We also performed microscopic observations of the device to elucidate the resistive switching mechanism during the cyclic application of $V_{2,4}$. The variation in the oxygen

vacancy distribution, which accounts for the change in the conductance state of TiO_{2-x} , can be visualized by exploiting the electro-coloring phenomenon.^{50,51} The local concentration of oxygen vacancies is well reflected in the color of the TiO_{2-x} crystal; a region with a higher (lower) concentration of oxygen vacancies appears bluish (colorless)²⁰. Our previous electron energy loss spectroscopy studies have demonstrated that reduced Ti valence states in the colored region are responsible for the occurrence of the HCS.^{19,21} **Figures 2c** and **2d** show optical micrographs of the device at the stages of $V_{2,4} = +8$ V and -8 V in the second cycle, respectively. The observed colored (colorless) region around T1 and T3 shown in **Figure 2c** (**2d**) indicates accumulation (depletion) of oxygen vacancies due to positive (negative) $V_{2,4}$ application, well reflecting the measured conductance increase (decrease) between T1 and T3. Thus, we can establish a direct correlation between the two-dimensional redistribution of oxygen vacancies and the resistive switching properties.

2.2. Numerical simulation of two-dimensional oxygen vacancies and electron conduction

Several numerical modeling approaches have been followed to date to gain a physical understanding of resistive switching. In principle, a model based on numerical solutions of coupled drift-diffusion equations for electrons and ions in a doped semiconductor thin film provides physical insights into the dynamics of electronic and ionic transport processes.⁵²⁻⁵⁴ For our four-terminal devices, to verify the two-dimensional redistribution behavior of oxygen vacancies, *i.e.*, *n*-type dopants, through the drift and diffusion model, we performed numerical simulations based on the finite element method. **Figures 3a** and **3b** show simulation results for the dopant distribution modified by applying a voltage with the same waveform as that in **Figure 1b**. After applying V_M (V_m), the dopants accumulate at the peripheries of T1 (T2) and T3 (T4) and are distributed around them, shown in light green color. These simulation results well reproduce the observed features of the colored regions in **Figures 2c** and **2d**.

Figures 3c and **3d** depict the simulated current density distributions subjected to an applied read voltage V_{read} for the dopant distributions shown in **Figures 3a** and **3b**, respectively. When dopants accumulate between T1 and T3, higher current density areas

bridge T1 and T3, whereas no such areas are formed in the case of accumulation between T2 and T4, which is also consistent with the experimentally measured conductance states, as shown in **Figures 2a** and **2b**. This agreement between the simulated and experimentally observed behavior validates our proposed model based on two-dimensional oxygen vacancy redistribution as a mechanism for the resistive switching phenomena occurring in the four-terminal device.

2.3. Tunable spike-timing-dependent plasticity

Next, we tried to impart synaptic functions to the four-terminal memristive device. In this study, we applied voltage pulses V_1 to T1, with T3 grounded, to modulate the conductance G_{1-3} . This voltage application protocol emulates the synaptic plasticity between pre- and post-synapse. In addition, gate voltage pulses V_{gate} were applied to both T2 and T4, being synchronized to V_1 to mimic the signal transmission from interneurons or a neuromodulator, as illustrated in **Figure 4**.

STDP is a Hebbian synaptic learning rule that is often demonstrated in biological experiments.^{55,56} The synaptic weight reflecting the strength of the connection between neurons is modulated by pre- and post-synaptic activities and depends on the relative timing of the pre- and post-synaptic spikes. In other words, the synaptic weight change Δw is a function of Δt , where Δt represents the time difference between two spikes. In canonical asymmetric Hebbian STDP, when the pre-spike precedes the post-spike ($\Delta t > 0$), the connection of synapses is strengthened, while when the post-spike precedes the pre-spike ($\Delta t < 0$), the connection is weakened. Other kinds of Hebbian STDP have been confirmed in biological systems as well, such as asymmetric anti-Hebbian and symmetric Hebbian rules.⁵⁷⁻⁶¹

Implementation of STDP as the functionality of artificial synapses has been demonstrated in various types of memristive devices,⁶²⁻⁶⁶ where two-terminal devices are commonly utilized for inputting the pre- and post-spikes to change the device conductance as a synaptic weight. However, synaptic weight modulation in STDP has so far been rarely demonstrated in a hetero-synapse configuration. Here, we attempted to implement STDP in our four-terminal devices and perform gate modulation of STDP characteristics. To reproduce this behavior, we have to utilize pre-/gate- and post- spikes that have both

positive and negative components in the waveform. Arbitrary wave forms that meet the condition can be used as pre-/gate- and post- spike. In this study we used spikes that are formed by simple combination of positive and negative square pulses. The designed spike pulse schemes for pre- and post-spikes and a gate spike are shown in **Figures 5a** and **5b**, respectively. The spike composed of positive and negative voltage pulses ($V_1 = \pm 7$ V, 25 ms) was used for the pre/post-spikes and applied to the device with a time difference Δt . The gate spike pulse with a voltage amplitude V_{gate} as a variable (the upper part of **Figure 5b**) was applied for the gate modulation, while the pulse width and the timing were the same as those for the pre-spike. The total effective voltage pulses on the device are shown in the lower parts of **Figures 5a** and **5b**, and in practice, these pulse schemes were applied to T1 for the input of the pre/post-spikes and to both T2 and T4 for the gate modulation while T3 was grounded. **Figure 5c** shows the detailed voltage application protocol. First, a read voltage of 100 mV was applied to T1 to measure the conductance between T1 and T3, then 12 V was applied to the gate terminals (T2 and T4) for 50 s to concentrate oxygen vacancies between T1 and T3, and voltage pulses were then applied to T1 to initialize the device conductance to a certain value. Next, as the first STDP process, the V_1 spike was applied to T1 and the device conductance was measured before and after the application. After initialization, as the second STDP process, the V_{gate} spike was applied to T2 and T4 simultaneously with V_1 application. This measurement cycle was repeated with changing Δt .

Figures 5d to **5k** show the observed variations of synaptic weight Δw as a function of Δt , where

$$\Delta w = \frac{G_{\text{after}} - G_{\text{before}}}{G_{\text{before}}} \times 100 [\%]. \quad (1)$$

Figure 5d shows the result for the case where no gate spike was applied, in which canonical asymmetric Hebbian STDP is clearly reproduced, featuring the occurrence of potentiation (positive Δw) when $\Delta t > 0$ and depression (negative Δw) when $\Delta t < 0$. The probable physical cause of the observed response behavior is that, when Δt is positive, effective negative voltage pulses are applied to T1 so that the oxygen-vacancy-accumulated region extends over the T1 side, leading to a conductance increase. On the other hand, when Δt is negative, the effective positive voltage pulses

applied to T1 could repel oxygen vacancies, resulting in a conductance decrease. We note that the STDP characteristics can be modulated systematically by simultaneous application of the gate spike. With increasing V_{gate} , $|\Delta w|$ decreases irrespective of the polarity of Δt , and further increasing V_{gate} to 7 V (**Figure 5k**) results in asymmetric anti-Hebbian STDP in which the synaptic weight change occurs in an inverse relationship to spike timing compared to canonical Hebbian STDP: post-leading-pre spike and pre-leading-post spike pairings lead to potentiation and depression, respectively. Since the V_{gate} spike scheme is determined by the employed values of V_{gate} and Δt as shown in **Figure 5b**, the V_{gate} increase causes an increase in the effective negative (positive) voltage pulse relative to the positive (negative) pulse for a positive (negative) Δt . Such an effective negative (positive) voltage pulse could induce enhanced dispersion (accumulation) of oxygen vacancies in the region between T1 and T3 and thus decrease (increase) the device conductance (Figure S5). In this way, the gate spike systematically tunes the amount of synaptic weight induced by STDP and controls the STDP rule in the forms of canonical Hebbian and anti-Hebbian as well as in the form of transient states.

It is noteworthy that the above gating operation plays a role in mimicking the neuromodulatory control of STDP, which often occurs in biological synapses. Indeed, STDP is not only controlled by temporal states during the spiking activity of pre- and postsynaptic neurons, but also by neuromodulation. Recent neurophysiology results have revealed that several neuromodulators, such as dopamine, noradrenaline, and monoamine, have a distinct influence on changing the polarity, shape and timing windows of STDP⁶⁷⁻⁷⁵ and provide mechanisms for adjusting the synaptic weight to regulate the plasticity of synapses.⁷⁶ In biological nervous systems, implementation of not only canonical Hebbian but also anti-Hebbian STDP may prove advantageous for effective association and reinforcement learning⁷⁷⁻⁸¹, which could also enhance the computational power of neural networks composed of artificial synapses. In addition, it should be noted that the transient states of conversion from canonical Hebbian to anti-Hebbian, as seen at a V_{gate} of 5V in **Figure 5i**, can be regarded as silent synapses in which neurotransmission does not produce a response in the receiving cell. The maturation of silent synapses via unsilencing and stabilization is known to be a fundamental step in the development and refinement of neural networks in the brain.⁸²⁻⁸⁷ On the other hand, in terms of machine learning using

deep neural networks, the formation of silent synapses corresponds to “dropout,” which is one effective technique used for regularization of neural network models: the nodes are “dropped” along with the connections from the neural network during training to prevent the model from overfitting.⁸⁸ As shown in Ref. 88, compared to other techniques such as solely introducing L1 and L2 regularization and model combination, dropout considerably improves the performance of standard neural networks on various types of data sets. In software based systems, one of the drawbacks of dropout is that it increases training time. Although this might also be the case in the hardware systems, our device enables modulation of STDP behaviors by gate operation and this allows arbitrary control of dynamic on/off state of artificial synapses as silent synapses. Such utilization is not achieved by other synaptic device hardware proposed so far. Thus, the configuration employed for the four terminals and the conductance control based on the oxygen vacancy distribution are demonstrated to have diverse functionalities as building blocks of neuromorphic computing systems.

2.4. Habituation and sensitization: heterogeneous control of synaptic plasticity

Extensive pioneering studies on habituation and sensitization have been conducted in biological systems, in particular, for the siphon- and gill-withdrawal reflex in the marine mollusc *Aplysia*. In the specific interneuronal circuit, which has been precisely identified, sensory neurons innervating the siphon are directly connected to motor neurons and indirectly connected through several excitatory and inhibitory interneurons.⁸⁹⁻⁹² Habituation occurs, with repeating presentation of a stimulus, in the form of a gradual decrease in the response to stimulus when the *Aplysia* learns that the stimulus is harmless. This learning results from a decrease in the strength of synaptic connections between sensory and motor neurons.^{93,94} On the other hand, in sensitization,⁹³ a general increase in response to neutral stimuli occurs, for example, after presentation of a noxious stimulus in the tail of *Aplysia*.^{95,96} This behavioral response is caused by an increase in the strength of heterogeneous synapses in the set of connections between sensory and motor neurons.^{1,94}

Here, we report the imparting of habituation and sensitization properties to a four-terminal device by the voltage application protocol illustrated in **Figures 6a** and **6b**, respectively. In this experiment, voltage pulses V_1 are applied to T1, mimicking neutral

inputs from the sensory neuron of the siphon, while T3 is grounded. Positive and negative V_1 applications correspond to potentiation and depression (P/D) operations, respectively. The behavioral response of the motor neuron of the gill is characterized by the conductance G_{1-3} between T1 and T3. As shown in **Figure 6a**, to implement the property of habituation, pulses of negative gate voltage V_{HG} are applied to both T2 and T4, as mimicking inputs from inhibitory interneurons, simultaneously with every potentiation operation in the P/D cycles. In the case of the sensitization shown in **Figure 6b**, the stimulation process is conducted prior to the P/D operation, in which pulses of positive gate voltage V_{SG} are applied to both T2 and T4 under a negative constant voltage application to T1, as mimicking inputs from interneurons connected to the tail being presented with a noxious stimulus. Note that V_{HG} is applied only in the stimulation process and not in the P/D processes.

Figure 6c shows the variation in G_{1-3} , normalized by the first maximum of the potentiated conductance, in 20 cycles of P/D operation under several conditions of V_{HG} application, in which consecutive 100-ms-wide pulses of V_1 with +10 and -10 V are applied for potentiation and depression, respectively. In the case where V_{HG} is not applied (n/a), a stable P/D waveform is observed. On the other hand, when V_{HG} is applied, the conductance increase becomes smaller in potentiation, and the conductance decreases more in depression, compared to the n/a case. The amplitude of the P/D waveform gradually decreases with increasing number of cycles, mimicking habituation, in which the response is decreased due to repeated stimuli. As an indicator, the degree of habituation is estimated using the amplitude of the conductance change in the first potentiation and that in the last potentiation, and the results are shown in **Figure 6d**. The degree of habituation clearly depends on the gate voltage: it increases with negatively increasing V_{HG} , indicating the feasibility of gate tuning for the habituation property.

Representative results for sensitization are shown in **Figure 6e**. Variations of G_{1-3} were measured at non-applied gate voltage (n/a) and different V_{SG} in the range from 2 to 10 V; 100-ms-wide stimulation and P/D pulses were applied, respectively, 5 and 10/10 times after setting the initial G_{1-3} to LCS (~ 1.6 mS). In the n/a and relatively low V_{SG} cases, there was little change in G_{1-3} . As V_{SG} increased to 6 V, G_{1-3} was observed to increase slightly in both the stimulation and P/D processes. A further increase in V_{SG} initially led to a sharp increase in the stimulation process, which eventually dropped to a level comparable to the

6 V case. However, in the subsequent potentiation process, G_{1-3} increased sharply and significantly. **Figure 6f** summarizes the G_{1-3} values after initialization, stimulation, and application of 10 potentiation pulses as a function of V_{SG} . Note that G_{1-3} increases sharply with increasing V_{SG} , mimicking sensitization: the stronger the stimulation, the higher the response.

On the basis of the oxygen vacancy distribution in the active zone of the four-terminal device, we infer the mechanisms behind the implemented habituation and sensitization behavior of the device. The application of a negative (positive) gate voltage induces a distribution of oxygen vacancies that is diluted (condensed) compared to the n/a case, in the region between T1 and T3 where the conductance change occurs. The diluted (condensed) oxygen vacancy distribution gives rise to a smaller (larger) conductance change per P/D pulse in habituation (sensitization), since the rate of conductance change depends on the concentration of oxygen vacancies as a dopant.²¹ Therefore, with increasing absolute value of gate voltage, a larger modulation of the oxygen vacancy distribution is achieved, resulting in a smaller conductance variation in habituation and a larger attainable conductance in sensitization.

2.5. Pavlovian conditioning

A typical example of associative learning was described by Ivan Pavlov,⁹⁷ known as Pavlovian conditioning. In this case, a dog is observed to salivate whenever it senses the presence of food. Then, in a conditioning process, a bell is rung every time the dog is fed. As a result of the conditioning, the dog starts to salivate on hearing the bell even when no food is given. Thus, by feeding the dog and ringing the bell with temporal contiguity, the dog learns to associate the bell sound (neutral stimulus) with food (unconditioned stimulus). The behavior of Pavlov's dog is summarized in the truth table shown in **Table 1**.

Here, we performed a series of electrical measurements to implement Pavlovian learning to the four-terminal memristive device. As shown in **Figure 7a**, applications of voltages to T1 and T4 (V_1 and V_4 , respectively) were assigned to the bell sound and food, respectively, and the resultant conductance G_{1-3} between T1 and T3 was assumed to represent the response of a dog. We first conducted an initialization process, where a set of write voltages were applied to both T3 and T4 with both T1 and T2 grounded, to adjust

G_{1-3} to a certain value of LCS and the conductance G_{4-3} between T4 and T3 to a certain value of HCS. A high G_{4-3} corresponded to the dog's natural reaction to food, i.e., the unconditioned stimulus. Then, voltage pulses of V_1 were applied repeatedly to simulate the bell sound. At every tenth application of V_1 , a voltage pulse V_4 was applied simultaneously with the food. G_{1-3} was measured after each application of V_1 by applying a non-disturbed voltage to T1, with T3 grounded.

Figure 7a shows an application sequence at a V_1 of -9 V and a V_4 of 9 V as well as the resultant conductance values of G_{1-3} . In the first series of V_1 pulse application before V_4 application, G_{1-3} showed no significant change from its initial value of ~ 0.6 mS. After the simultaneous application of V_1 and V_4 , a slight increase was observed in G_{1-3} . Then, the third simultaneous application of V_1 and V_4 at a pulse number of 30 caused G_{1-3} to be increased sharply, which maintained high values afterwards only with the application of V_1 . This result points to the phenomenon of conditioning: the dog associates the bell sound with food and learns to react to the bell sound alone. We also verified the phenomenon of forced forgetting, where G_{1-3} could be reset to its initial value (~ 0.6 mS) by the initialization process at a pulse number of 40 and then no increase in G_{1-3} occurred with the V_1 pulse. The conditioning state was recalled by the subsequent simultaneous application of V_1 and V_4 at pulse numbers of 50 and 70. Note that only a single trial of simultaneous V_1 and V_4 application is enough for the second and third conditioning processes, whereas three trials are required for the first conditioning process. This, however, can be tuned by changing the applied voltage. One set of measurement results is shown in **Figure 7b** with -6.5 V and 6.5 V used for V_1 and V_4 , respectively. In this case, conditioning was completed after three simultaneous V_1 and V_4 applications for every conditioning process after initialization. Thus, the required number of times for conditioning is found to be controllable by changing the amplitude of V_1 and V_4 . From these experiments, we conclude that the tunable Pavlovian conditioning scheme can successfully be implemented in four-terminal memristive devices.

The oxygen vacancy distribution in the device during the above Pavlovian conditioning process was also examined through electro-coloring phenomena. **Figure 7c** shows typical optical micrographs of the electrically active zone of the device subjected to voltage applications according to the protocol shown in **Figure 7a**. At the initial stage (pulse

number: 0) just after initialization, dark blue regions were observed, mainly around the tips of T3 and T4. After the first conditioning process (pulse number: 30), a strongly colored region was generated at the tip of T1. A similar contrast change in the colored region was confirmed after the second initialization (pulse number: 40) and conditioning (pulse number: 50), where, once again, dark blue T3 and T4 regions and strongly colored T1 regions were observed, respectively. As described above, since the coloration in TiO_{2-x} stems from the increase in the oxygen vacancy concentration at the site, the observed color change reflects a modulation of the oxygen vacancy distribution. During initialization, the electric field causes the oxygen vacancies around T1 and T2 to drift towards T3 and T4 (Figure 7d). The resulting dilution (condensation) of oxygen vacancies around T1 (T3 and T4) likely leads to low G_{1-3} (high G_{4-3}). On the other hand, during conditioning, the effectively large electric field directed from T4 to T1, induced by the simultaneous application of negative V_1 and positive V_4 , causes oxygen vacancies to drift and densely accumulate around T1, thereby leading to a high G_{1-3} . Thanks to these procedures, the conductance state of G_{1-3} can be switched between LCS and HCS.

The present results directly indicate that variation of the oxygen vacancy distribution in the device's active region plays a crucial role in changing the electrical conductance between the electrode terminals. This gives rise to a rich variety of synapse-based connectionism, which demonstrates that associative learning was successfully implemented as a hetero-synaptic function in a single four-terminal memristive device. In terms of the device structure, our four-terminal memristor may apparently be similar to dual-gated transistors or prototypic JFET models. However, working principle of our device is based on two-dimensional redistribution of oxygen vacancies and this brings striking versatility of memristive characteristics. In synaptic operations mentioned above, not only electrical interaction between two orthogonal pairs of electrodes, interaction between neighboring electrodes that brings oblique modification of vacancy distribution is exploited as in the case of Pavlov's dog demonstration (Fig. 7). This property variation can be further extended by changing shape, number and configuration of electrodes. Exploration of different device configurations and networking will be subjects of interest in future works.

3. Conclusions

In this work, we fabricated four-terminal memristive devices consisting of epitaxial TiO_{2-x} thin films, and characterized their conductive switching properties. Control of memristive properties through the two-dimensional oxygen vacancy distribution enables a rich diversity of artificial synaptic devices, in particular, hetero-synaptic modulation as a higher-order synaptic function. Behavioral habituation/sensitization and STDP were demonstrated, whose characteristics were successfully tuned by the application of a gate voltage to the device, mimicking the roles of hetero-synaptic input signals or neuromodulators. In addition, gate-tunable Pavlovian conditioning (associative learning) was implemented in a single memristive device without the need for transistors or other circuit elements. These results prove the strong capability and versatile functionality of our four-terminal memristive device in which conductance modulation is based on the modification of the two-dimensional distribution of oxygen vacancies in the TiO_{2-x} thin film. Since the device structure is relatively simple, the four electrode terminals can be scaled down to a submicron size and readily arranged into a multi-terminal (more than four) configuration. This facilitates a further extension of functionalities for an advanced synaptic device.

4. Experimental Section

Fabrication of memristive device

Epitaxial thin films of TiO_{2-x} were prepared on non-doped insulative rutile- $\text{TiO}_2(001)$ single-crystal substrates by pulsed laser deposition (PLD) using a Nd:YAG laser ($\lambda=266$ nm). During deposition, the substrate temperature was kept at 500°C and the oxygen partial pressure was kept constant. The laser fluence was controlled to obtain a growth rate ranging from 2.5 to 3.0 nm/min. The thickness and resistivity of the TiO_{2-x} epitaxial thin films were varied by changing the deposition time and the oxygen partial pressure. After film deposition, planar devices with four-terminal electrodes were fabricated by sputtering Pt films on the TiO_{2-x} film surface through a metal mask. Each electrode was verified in advance to have an ohmic contact with the TiO_{2-x} film, which typically occurs in

highly-doped semiconductor films.

Electrical measurements

All electrical measurements were performed using a prober station inside a vacuum chamber at a base pressure below 10^{-3} Pa and a Keysight B1500A semiconductor device analyzer. The conductances of the devices were calculated from the currents measured between T1 and T3 at an applied voltage of 1 V to investigate the conductance variation during consecutive voltage applications and 100 mV to examine gate voltage effects on the synaptic plasticity of the device.

Simulation model

We performed a two-dimensional (2D) finite element simulation considering the drift and diffusion of n -type dopants (oxygen vacancies), and evaluated the dopant density distribution between terminals. First, the carrier density distributions, n and p , and potential distribution, φ , were determined by solving the carrier continuity equations (2) and (3) and the Poisson equation (4). Then, we solved the continuity equation (5) in consideration of the drift and diffusion of dopants to obtain a new donor density distribution, N_d .

$$\nabla \cdot (qp\mu_p\nabla\varphi + qD_p\nabla p) = 0 \quad (2)$$

$$\nabla \cdot (qn\mu_n\nabla\varphi - qD_n\nabla n) = 0 \quad (3)$$

$$\nabla^2 \varphi = -\frac{q}{\epsilon}(p - n + N_d - N_a) \quad (4)$$

$$q \frac{dN_d}{dt} = \nabla(qN_d\mu_i\nabla\varphi - qD_i\nabla N_d) \quad (5)$$

The modulation of the donor density distribution was evaluated under specific voltage application conditions by solving Eqs. (2), (3), (4), and (5) recursively. For the present simulation, the following parameters were used.

Parameter		Value	Units
permittivity of vacuum	ϵ_0	8.854×10^{-12}	F/m
elementary charge	e	1.602×10^{-19}	C

Boltzmann constant	k_B	1.381×10^{-23}	J/K
temperature	T	300.0	K
thermal voltage	$v_0 = k_B T / e$	2.552×10^{-2}	V
bandgap energy	E_g	3.000	eV
relative permittivity	ϵ_r	10.00	-
electron mobility	μ_e	1.0×10^{-4}	m^2/Vs
hole mobility	μ_h	1.0×10^{-4}	m^2/Vs
conduction band density of states	N_c	2.8×10^{25}	$/\text{m}^3$
valence band density of states	N_v	2.8×10^{25}	$/\text{m}^3$
donor density in electrode	N_{d0}	5.0×10^{26}	$/\text{m}^3$
initial donor density of memristor	N_d^*	5.0×10^{25}	$/\text{m}^3$
acceptor density	N_a	5.0×10^{24}	$/\text{m}^3$

Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/xxxx>.

Additional experimental details and supporting figures; RHEED pattern of the epitaxially grown TiO_{2-x} film; dependence of resistive switching properties on TiO_{2-x} layer resistivity and thickness; endurance and retention properties of conductance states; supplementary figures for gate effects on the STDP property (PDF)

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Conflict of Interest

The authors declare no conflict of interest.

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Table 1. Truth table for Pavlovian conditioning before and after learning.

bell sound	food	response (before conditioning)	response (after conditioning)
0	0	0	0
0	1	1	1
1	1	1	1
1	0	0	1

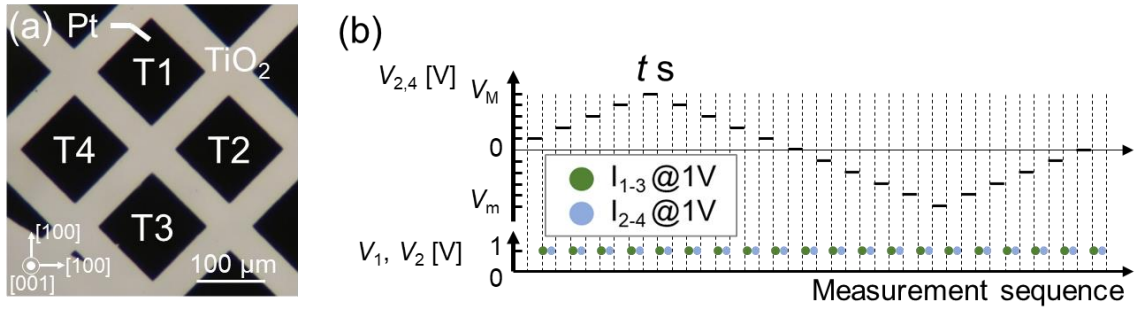


Figure 1

(a) Optical micrograph of a four-terminal planar memristive device fabricated on a TiO_{2-x} epitaxial thin film. (b) Time series of applied write voltage $V_{2,4}$ and read voltages V_1 and V_2 . I_{1-3} and I_{2-4} were measured at 1 V between consecutive steps of $V_{2,4}$ application.

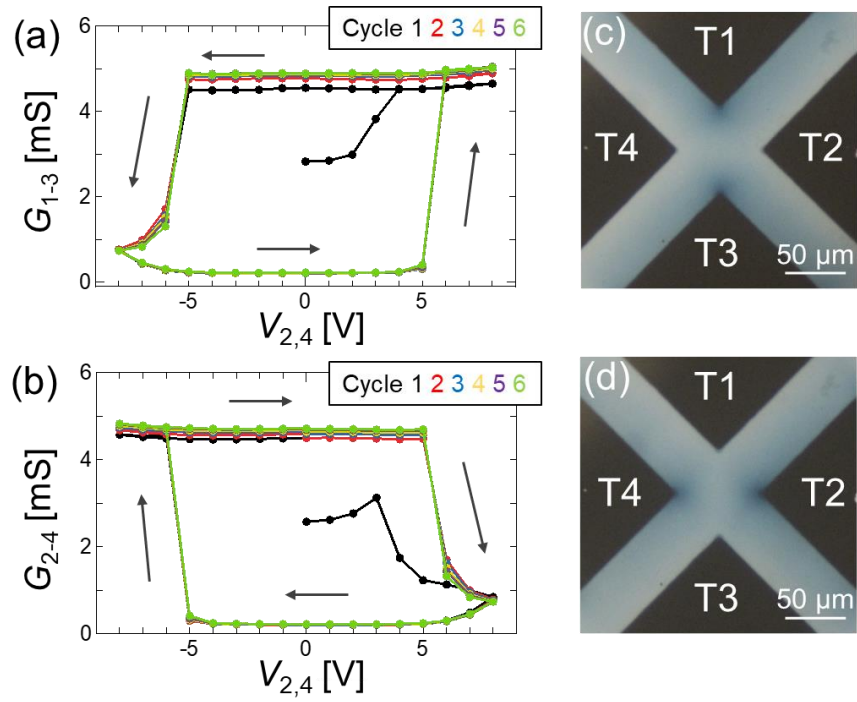


Figure 2

Variation of (a) G_{1-3} and (b) G_{2-4} with $V_{2,4}$. (c), (d) Optical micrographs showing internal structure of device just after application of (c) $V_{2,4} = 8$ V and (d) $V_{2,4} = -8$ V in cycle 2.

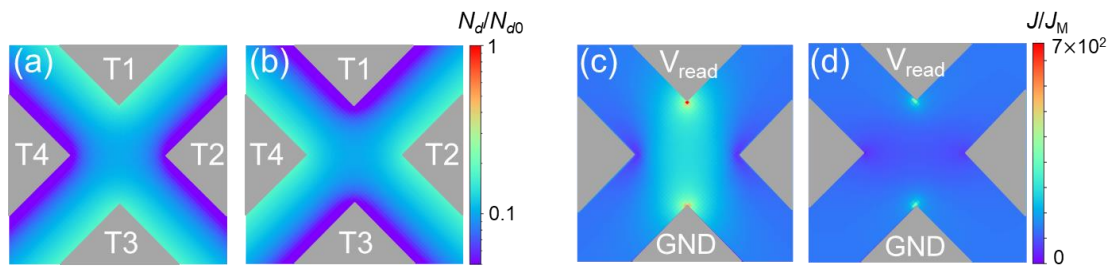


Figure 3

Simulation results for (a), (b) oxygen vacancy concentration (OVC) distribution and (c), (d) current density (CD) distribution, obtained at (a), (c) V_M and (b), (d) V_m . The calculated OVC and CD values were normalized by the N_{d0} (donor density in electrode) and the J_M (maximum current density), respectively.

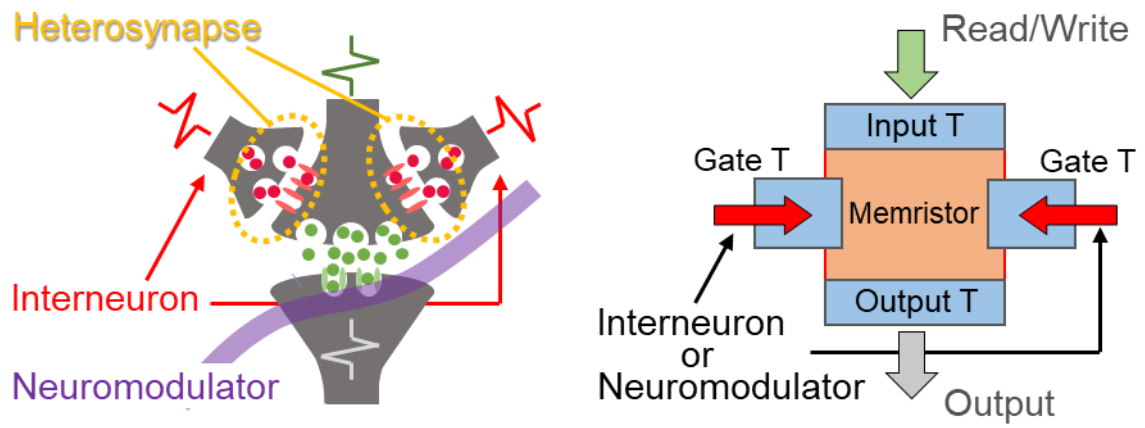


Figure 4

Schematics comparing a biological heterosynapse and neuromodulator (left) and a four-terminal memristive device (right).

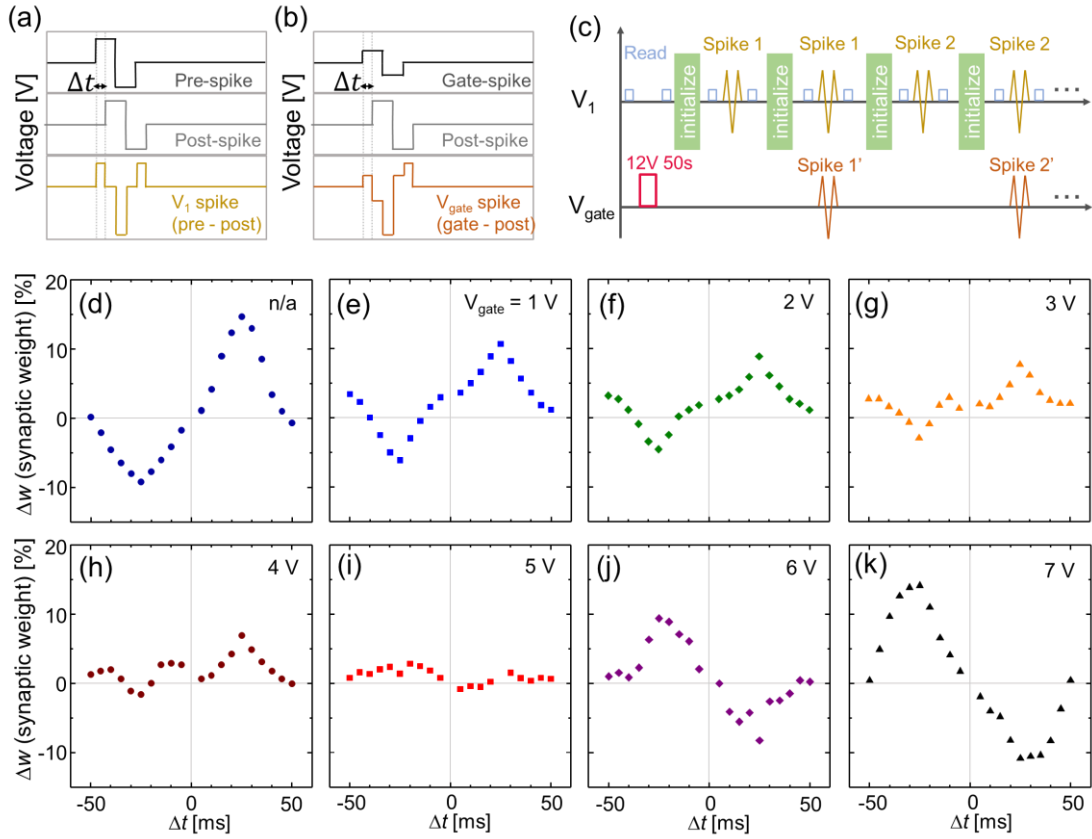


Figure 5

(a), (b) Designed spike pulse schemes used to impart STDP properties. The upper, middle, and lower parts in (a) show pre-spike, post-spike, and total effective voltage applied to T1, respectively. The upper, middle, and lower parts in (b) show gate-spike, post-spike, and effective voltage applied to T2 and T4. (c) Time series of applied voltage V_1 and V_{gate} . I_{1-3} and I_{2-4} were measured at 100 mV. (d)-(k) Experimental STDP results. The synaptic weight change Δw is plotted as a function of the time difference Δt for each V_{gate} .

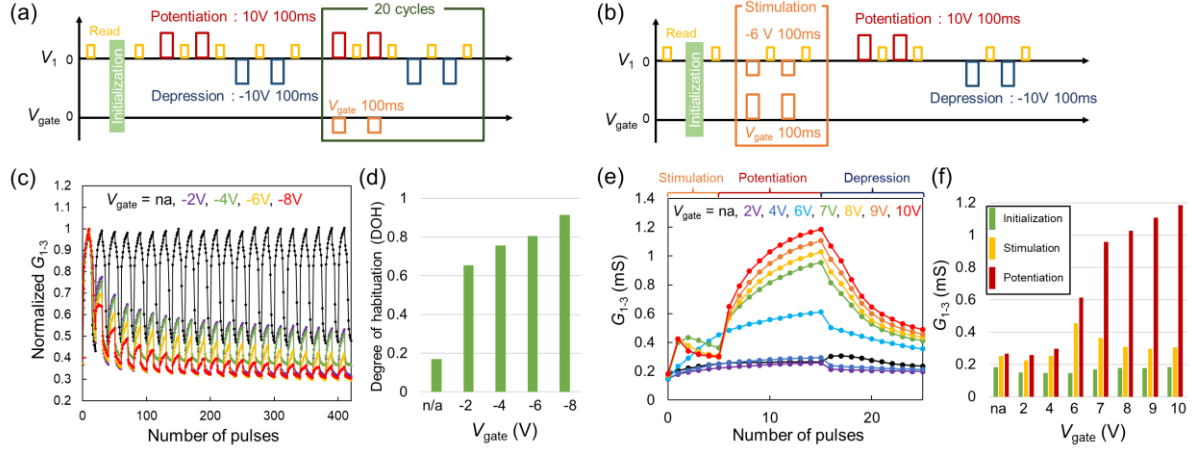


Figure 6

Voltage application protocol for (a) habituation and (b) sensitization. V_1 is applied to T1 for the P/D operation and V_{HG} is applied to both T2 and T4 simultaneously with V_1 in the habituation. The stimulation process with V_{gate} is employed prior to the P/D operation in the sensitization. (c) Modulation of G_{1-3} with $V_{HG} = n/a, -2, -4, -6, -8$ V, showing the habituation characteristics. (d) Degree of habituation (DOH) as a function of V_{HG} . DOH was derived from an equation; $DOH=1-(\Delta G_{1-3,LP})/(\Delta G_{1-3,IP})$ where $\Delta G_{1-3,LP}$ ($\Delta G_{1-3,IP}$) is the conductance change at the last (initial) potentiation process. (e) Modulation of G_{1-3} with $V_{SG} = n/a, 2, 4, 6, 7, 8, 9, 10$ V, showing the sensitization characteristics. (f) V_{SG} dependence of G_{1-3} of the initial state, after the stimulation, and after the potentiation.

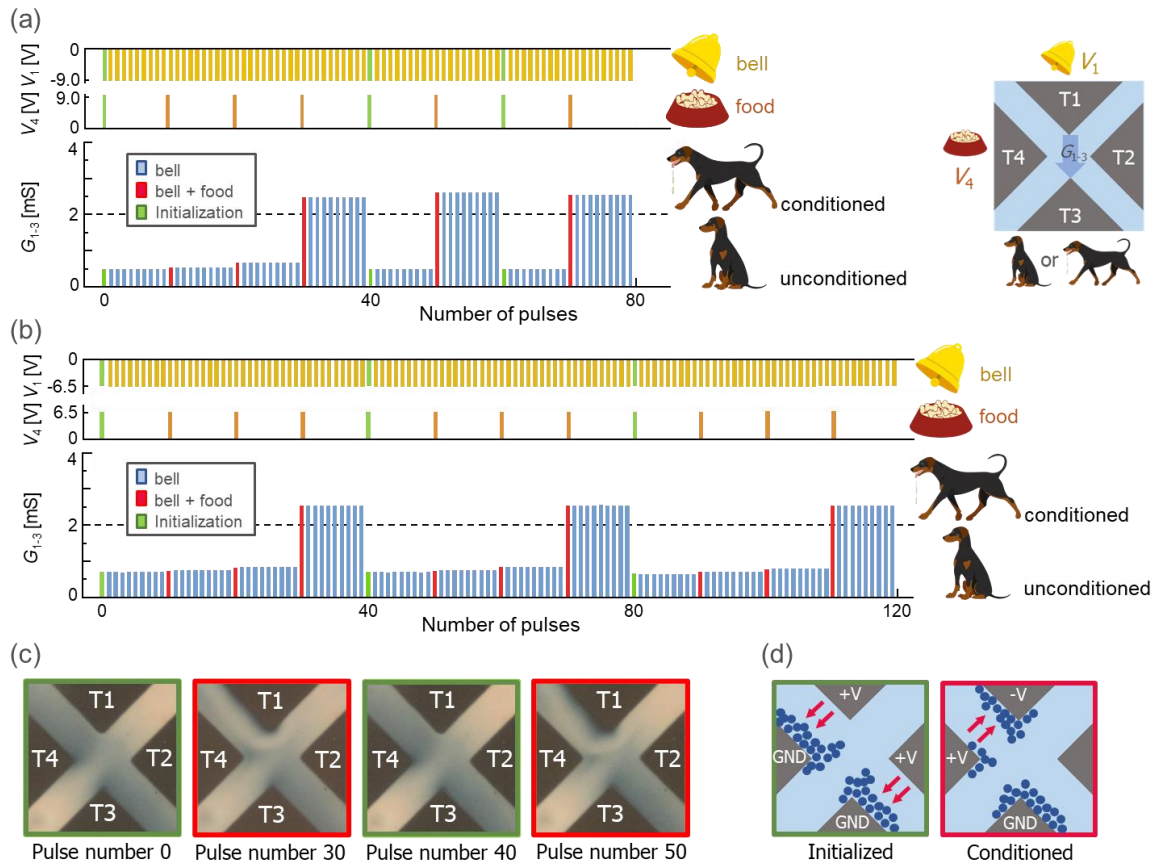


Figure 7

Voltage application protocol emulating Pavlovian conditioning and resultant conductance G_{1-3} change with voltage amplitudes of (a) $V_1 = -9$ V, $V_4 = 9$ V and (b) $V_1 = -6.5$ V, $V_4 = 6.5$ V. Inset of (a) illustrates correspondence between four-terminal memristive device and Pavlovian conditioning; V_1 corresponding to the bell sound (upper), V_4 to the food (middle), and G_{1-3} to the dog's response (lower). (c) Optical micrographs taken after voltage application, corresponding to 0, 30, 40, and 50 pulses in (a). (d) Schematics depicting oxygen vacancy distribution for initialized and conditioned states.

Table of Contents (TOC)/Abstract Graphic

